

## 1. General description:

Silicon Carbide Schottky diode in a TO252-2L plastic package, designed for high frequency switched-mode power supplies.



## 2. Features and benefits:

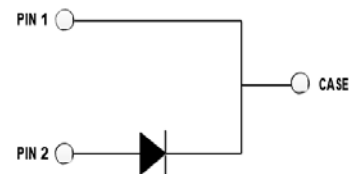
- Zero Reverse Recovery Current
- Positive temperature coefficient
- Temperature-independent performance
- High-speed switching
- Low switching loss
- Low heat dissipation requirements

## 3. Applications:

- Switching power supply
- Power factor correction
- Motor drive, traction
- Charging pile
- PC Silverbox

## 4. Typical Performance Diagrams and Package:

$V_{RRM}$	650	V
$I_F(135^{\circ}C)$	6	A
$Q_c$	12	nC



## 5. Ordering information:

Parts Number	Marking	Package	Vde min	IF max(A)	Pins	SPQ	Packaging
TSSiC065S002E	TSSiC065S002E	TO-252-2L	650V	2A	2	600	Tube

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## 6. Absolute Maximum Ratings

Parameter	Symbol	Value	Unit	Test Conditions
Reverse voltage (Repetitive peak)	$V_{RRM}$	650	V	$T_C = 25^\circ\text{C}$
Reverse Voltage (Surge peak)	$V_{RSM}$	650		$T_C = 25^\circ\text{C}$
Reverse voltage (DC)	$V_{DC}$	650		$T_C = 25^\circ\text{C}$
Continuous forward current	$I_F$	6.9	A	$T_C = 25^\circ\text{C}$
		3.3		$T_C = 135^\circ\text{C}$
		2.0		$T_C = 155^\circ\text{C}$
Surge non-repetitive forward current	$I_{FSM}$	22	A	$T_C = 25^\circ\text{C}$ , $t_p = 10\text{ms}$ , half Sine Pulse
Total power dissipation	$P_{TOT}$	33	W	$T_C = 25^\circ\text{C}$
$i^2t$ value	$\int i^2 dt$	2.42	$\text{A}^2\text{s}$	$T_C = 25^\circ\text{C}$ , $t_p = 10\text{ms}$
Operating temperature	$T_j$	-55~175	$^\circ\text{C}$	
storage temperature	$T_{stg}$	-55~175	$^\circ\text{C}$	

## 7. Thermal Characteristics

Parameter	Symbol	Values			Unit	Test Condition
		Min.	Typ.	Max.		
Thermal resistance	$R_{th(j-c)}$	/	4.5	/	$^\circ\text{C}/\text{W}$	

## 8. Electrical Characteristics $T_j = 25^\circ\text{C}$

Parameter	Symbol	Values			Unit	Test Condition
		Min.	Typ.	Max.		
DC blocking voltage	$V_{DC}$	650	/	/	V	$I_R = 100 \mu\text{A}$
Forward voltage	$V_F$	/	1.40	1.70	V	$I_F = 2\text{A}$ , $T_j = 25^\circ\text{C}$
		/	1.75	2.50		$I_F = 2\text{A}$ , $T_j = 175^\circ\text{C}$
Reverse current	$I_R$	/	0.5	10	$\mu\text{A}$	$V_R = 650\text{V}$ , $T_j = 25^\circ\text{C}$
		/	1	40		$V_R = 650\text{V}$ , $T_j = 175^\circ\text{C}$
Total capacitance	C	/	106	/	pF	$V_R = 0\text{V}$ , $f = 1\text{MHz}$
		/	13.1	/		$V_R = 200\text{V}$ , $f = 1\text{MHz}$
		/	11.6	/		$V_R = 400\text{V}$ , $f = 1\text{MHz}$
Total capacitive charge	$Q_C$	/	6.5	/	nC	$V_R = 400\text{V}$
Capacitance Stored Energy	$E_C$	/	1.0	/	$\mu\text{J}$	$V_R = 400\text{V}$

## 9. Typical Electrical Characteristics Curves

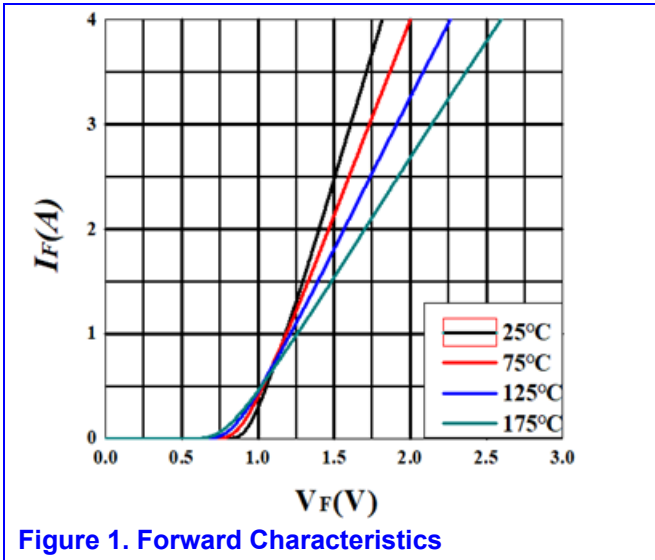


Figure 1. Forward Characteristics

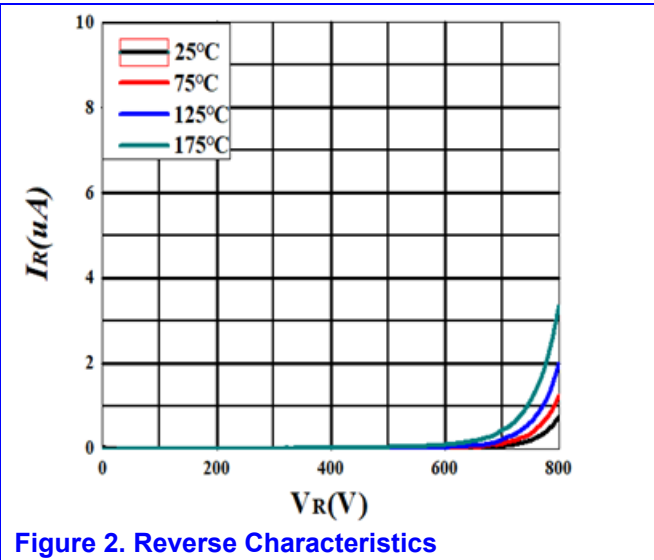


Figure 2. Reverse Characteristics

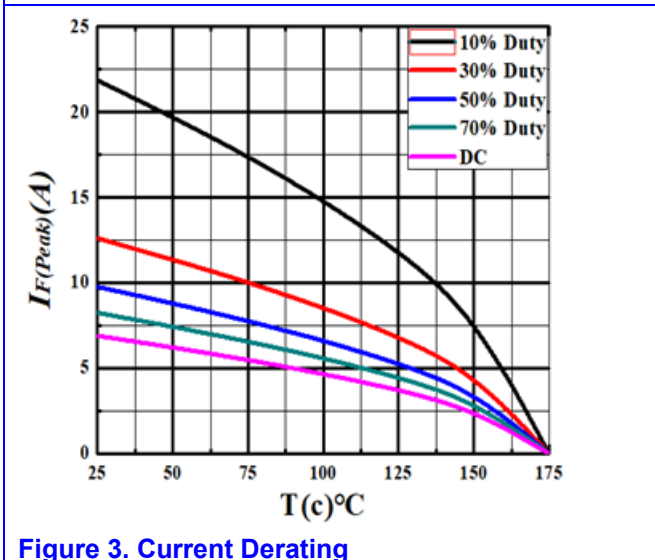


Figure 3. Current Derating

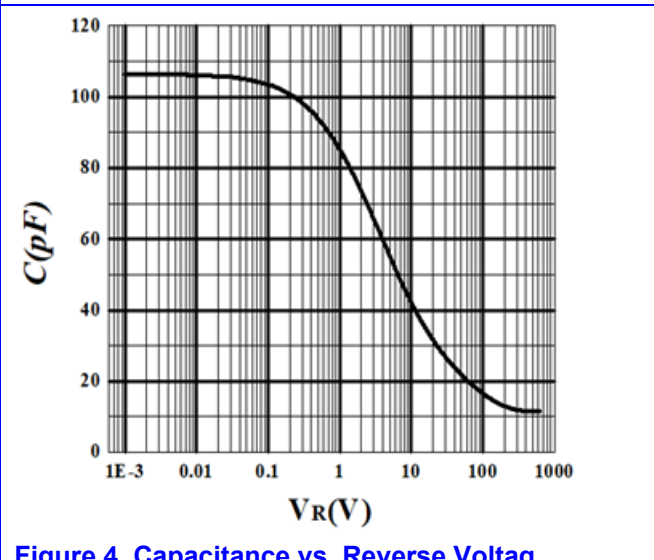


Figure 4. Capacitance vs. Reverse Voltage

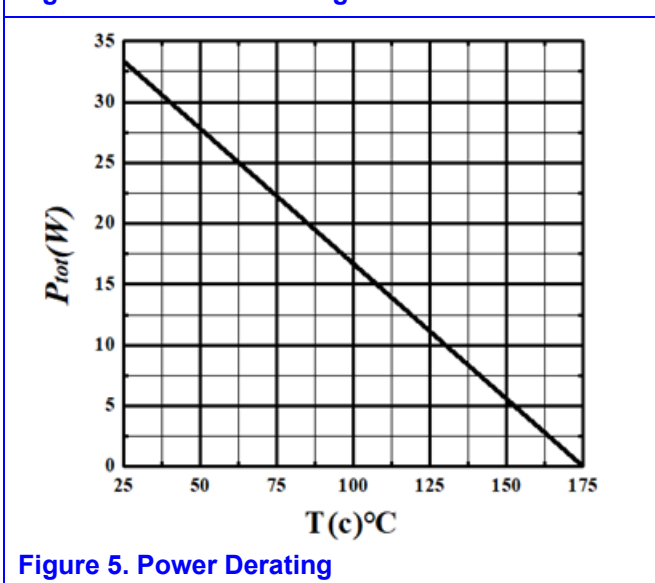


Figure 5. Power Derating

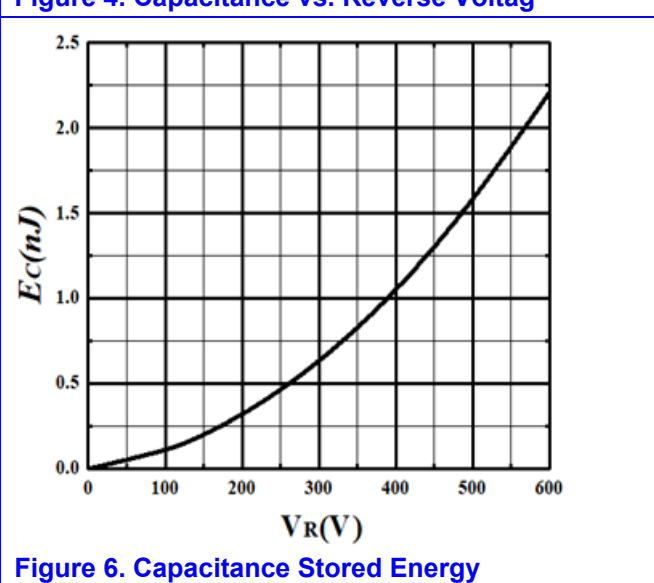


Figure 6. Capacitance Stored Energy

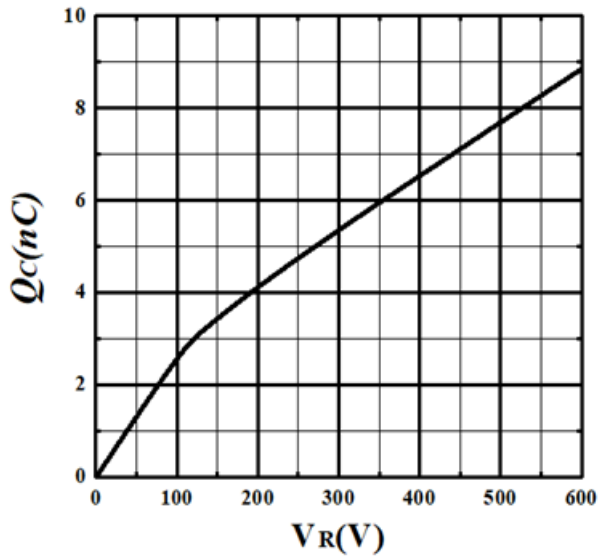
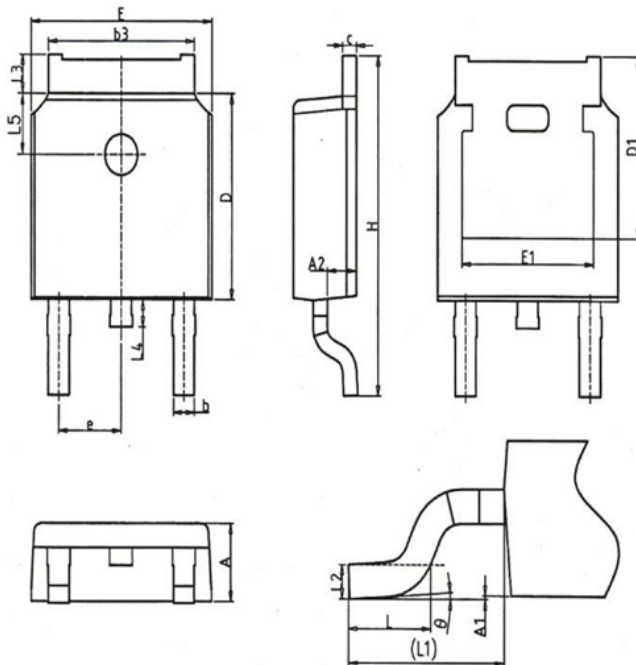


Figure 7. Total Capacitance Charge vs. Reverse Voltage

## 10. Package



SYMBOL	mm		
	MIN	NOM	MAX
A	2.2	2.3	2.38
A1	0	-	0.2
A2	0.97	1.07	1.17
b	0.68	0.78	0.9
b3	5.2	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.1	6.22
D1	5.30REF		
E	6.4	6.6	6.73
E1	4.63	-	-
e	2.286BSC		
H	9.4	10.1	10.5
L	1.38	1.5	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	-	1.28
L4	0.5	-	1
L5	1.65	1.8	1.95
θ	0°	-	8°

## 11. Ordering information

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Part Number	TSSiC065S002E
Package	TO-252-2L
Marking	TSSiC065S002E
Unit Quantity	600 EA
Packing Type	Tape Reel
RoHS	Yes

## 12. Notes

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SiC Schottky diode portfolio: <http://www.thrivesemi.com/>

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